Cagri Tanriover

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Summary

Lead data scientist with 20+ years of experience, specializing in transforming complex ideas into robust systems. Proven track record of shipping code to over 600 devices, filing [26 patents](), and mentoring 400+ engineers. Advised companies in Europe and co-founded a startup, demonstrating strong leadership and innovation in hardware and software-based product development.

Skills & Tools

Tools: Python, NumPy, pandas, scikit-learn, C, C++, Git, GitHub, mmWave radar, Wi-Fi, RFID, DSP.

Capabilities: dataset creation and management, project management, feature engineering, experiment design, managing field deployments, clear documentation, stakeholder management, hiring and interviews, data-driven decision-making, prototyping, AI, machine learning, statistics, ideation.

Work Experience

Staff AI Research Engineer and Scientist - Wireless Sensing

INTEL, Hillsboro, OR — February 2019 - December 2025

• Advised management on wireless research strategy through gap analysis and delivered presentations to internal business executives and external partners (e.g., Dell, Lenovo, HP), aligning with product management strategy and driving data-driven decision-making.

• Screened 20+ and interviewed 10+ candidates, contributed to team growth and strengthened team-building capabilities through effective hiring and interviews.

• Managed 2 EU Horizon and 2 university research projects, ensuring successful project delivery, clear documentation, and alignment with product delivery goals.

• Accelerated downstream ML to support Intel business unit engagements with external customers by creating Intel's largest RFID dataset (1,000 tagged items) and radar gesture set (600 labeled instances), leveraging feature engineering and dataset creation.

• Removed 3D-camera dependency, saving $300 per unit, by deploying a deep-learning CV pipeline using Python and demonstrating strong analytical skills.

• Cut training-data needs by 30% via sparse point-cloud selection in Point Transformer networks, with no accuracy loss, showcasing expertise in statistics and model optimization.

• Co-authored [7 publications and 4 patents]() in wireless sensing, contributing to intellectual property development.

• Mentored 400+ Intel engineers via a new patent training program, reviewed 1,700+ disclosures to strengthen IP strategy and foster a culture of innovation.

• Received top-tier inventor recognition (26 patents and multiple awards), highlighting a strong contribution to intellectual property and product development.

Staff Research Engineer and Scientist - Human Activity Recognition

INTEL, Hillsboro, OR — September 2016 - January 2019

• Advised management on research strategies in personalized learning and autonomous in-cabin passenger experiences, contributing to ideation and strategic planning.

• Received the [Division Recognition Award](#) for system deployment and tooling in two classrooms to enhance student and teacher experience, demonstrating project management and execution.

• Designed and implemented [pseudo-autonomous car experiments](#), engineered 500 GB of real-time road data to support ML research, led cross-functional teams, showcasing prototyping and analytical capabilities.

• Led a laptop-frustration study, collected 1 TB of in-the-wild audio-visual data (30 users) and secured pilot approval with findings, emphasizing data collection and analysis for user insights.

• Co-authored [6 publications and 10 patents](#) in human activity recognition and UX, contributing to the company's intellectual property portfolio.

Senior Embedded Systems Engineer - Wearables

INTEL, Istanbul, Turkey — August 2014 - August 2016

• Led technical engagements with 25 project partners (EzraTuba, Osteoid, DeFacto, etc.), created wearable prototypes and designs as proof of concept, demonstrating prototyping skills and stakeholder management.

• Established and fully equipped the [Intel Ignition Lab](#) as a collaborative experimentation space for all project partners, nurtured teamwork through technical leadership and fostering a growth mindset.

• Created the first [Intel Edison breakout board](#) for all wearable projects as part of strategic technical roadmap, contributing to technical infrastructure development.

• Engineered the [interactive Butterfly Dress](#) - featured among Intel's top 10 innovations (2016) and in [Intel Brand Book](#), partnered with marketing on multi-geo live demos, showcasing innovation and cross-functional collaboration.

• Developed [a smart wearable](#) to monitor spasticity in cerebral palsy patients in 3 months, demonstrating rapid execution from concept to prototype and a focus on accuracy and impact.

Co-founder and Embedded Solutions Architect

WHIZCOMM, Istanbul, Turkey — February 2011 - July 2014

• Built [WhizTrack](#) - a cellular + RFID wearable for real-time indoor personnel tracking - to solve multi-site operations visibility gaps.

• Launched a subscription pricing model for WhizTrack customers that optimized operation costs and improved predictability.

• Closed $50K in consulting revenue with European clients, expanding the company's footprint.

Digital Signal Processing (DSP) Architect

NOKTA ENGINEERING, Istanbul, Turkey — January 2010 - January 2011

• Defined the technical roadmap to differentiate products and align engineering with commercial goals.

• Designed and implemented Nokta's first digital signal processing pipeline, establishing the platform baseline for next-gen products.

Senior Software Engineer

ETHERSTACK, London, United Kingdom — March 2009 - December 2009

• Developed new display features for a mission-critical PMR handset, enhancing operator usability in the field.

• Secured FCC compliance for the handset line under regulatory constraints, clearing the path to commercialization.

Software Team Leader - Encryption

SOFTWARE RADIO TECHNOLOGY, Basingstoke, United Kingdom — March 2006 - January 2009

• Led a team of 3 software engineers to deliver end-to-end ETSI TETRA encryption features from spec to field operation.

• Converted ETSI TETRA mandatory encryption requirements into software specifications to guide development and validation.

• Integrated full security features deployed on 600 commercial handsets in the field.

• Achieved UK NCSC approval of all security features - a prerequisite to commercialize PMR handsets across Asia.

Senior Research and Development Engineer

HW COMMUNICATIONS, Lancaster, United Kingdom — January 2002 - February 2006

• Secured multiple European Telecommunications Standards Institute (ETSI) Specialized Task Force (STF) contracts, generating >$100K in new funding for the company.

• Authored the "Multimedia Exchange Layer" (Section 7.2) of ETSI Terrestrial Trunked Radio Access (TETRA) Enhanced Data Service (TEDS), contributing directly to standardization.

• Acknowledged by the ETSI chair for impactful contributions to TETRA/TEDS standards.

Education

PhD

Lancaster University, United Kingdom — October 1998 - February 2002

MSc

Lancaster University, United Kingdom — October 1997 - May 1998

BSc

Istanbul Technical University (ITU), Turkey — October 1993 - June 1997